

X-736 US
10/079,235



PATENT
Conf. No. 7201

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert O. Conn
Assignee: Xilinx, Inc.
Title: "Method to Produce a Factory Programmable IC Using
Standard IC Wafers and the Structure"
Serial No.: 10/079,235 Filing Date: 2/19/2002
Examiner: Shouxiong Hu Art Unit: 2811
Docket No.: X-736 US Conf. No.: 7201

Mail Stop Non-Fee Amendment
COMMISSIONER FOR PATENTS
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on November 6, 2003, Applicant submits the following amendments and remarks.

Amendments to the Claims begin on page 2 of this paper.
Remarks begin on page 11 of this paper.



2811

Modified 02-03

PTO/SB/21 (01-03)
Approved for use through 9/30/00. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application / Conf. No.	10/079,235 / 7201
	Filing Date	February 19, 2002
	First Named Inventor	Robert O. Conn
	Examiner Name	Shouxiang Hu
	Art Unit	2811
	Patent No.	
Mail Stop: NON-FEE AMENDMENT	Attorney Docket Number	X-736 US
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309	Reg. Number 51,959
Attn: Justin Liu	(Customer Number)	
Signature		
Date	February 6, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date: February 6, 2004			
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